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By: Madhus Nall

Date: July 23, 01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.
Applic. No. : 09/313,424 ✓
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Examiner : Brook Kebede
Group Art Unit : 2823

2014

A M E N D M E N T under 37 C.F.R. § 1.116

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the final Office action dated March 23, 2001,
kindly amend the above-identified application as follows:

In the Claims:

Cancel claims 7-9.

Claim 10 (amended) A method of fabricating a semiconductor
configuration, which comprises the following steps:

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